

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****Applicant(s):** Shreesh Narasimha, et al.**Examiner:** Quang D. Vu**Serial No.:** 10/763,308**Art Unit:** 2811**Filed:** January 23, 2004**Docket:** FIS920020200US2 (16106A)**For:** CMOS DEVICE INTEGRATION  
FOR LOW EXTERNAL RESISTANCE**Dated:** September 27, 2004**Confirmation No.:** 6529Commissioner for Patents  
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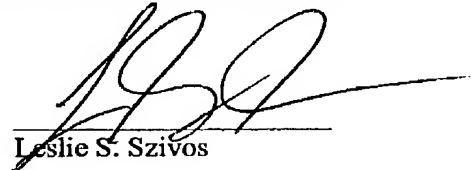
In response to the Office Action dated June 25, 2004, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

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**CERTIFICATION OF FACSIMILE TRANSMISSION**

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Dated: September 27, 2004

  
Leslie S. Szivos

400 Garden City Plaza, Suite 300  
Garden City, New York 11530  
(516) 742-4343 - Telephone  
(516) 742-4366 - Facsimile  
e-mail: intprop@ssmp.com

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<b>Phone:</b>		<b>Date:</b>	9/27/2004
<b>Re:</b>	U.S. Serial No. 10/763,308 Group Art Unit: 2811 Confirmation No: 6529 Docket No: FIS920020200US2 (16106A)	<b>CC:</b>	

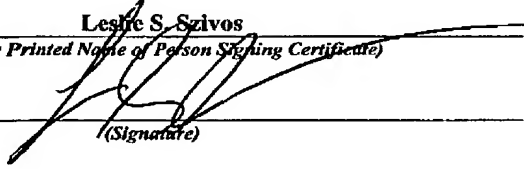
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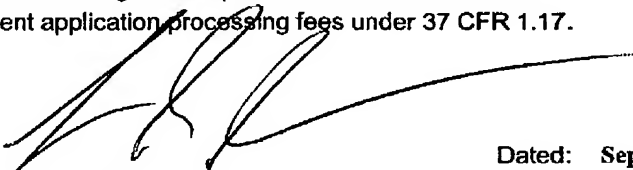
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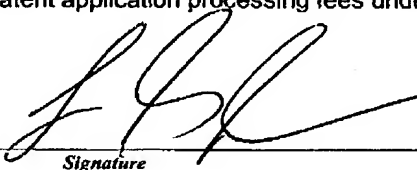
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<b>CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)</b>			<b>Docket No.</b>
Applicant(s): Shreesh Narasimha, et al.			FIS920020200US2 (16106A)
<b>Application No.</b> 10/763,308	<b>Filing Date</b> January 23, 2004	<b>Examiner</b> Quang D. Vu	<b>Group Art Unit</b> 2811
Invention: CMOS DEVICE INTEGRATION FOR LOW EXTERNAL RESISTANCE			
Confirmation No.: 6529			RECEIVED CENTRAL FAX CENTER  SEP 27 2004
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P18/REV02

<b>AMENDMENT TRANSMITTAL LETTER (Large Entity)</b>				<b>Docket No.</b>	
Applicant(s): <b>Shreesh Narasimha, et al.</b>				<b>FIS920020200US2 (16106A)</b>	
<b>Application No.</b>	<b>Filing Date</b>	<b>Examiner</b>	<b>Customer No.</b>	<b>Group Art Unit</b>	<b>Confirmation No.</b>
10/763,308	January 23, 2004	Quang D. Vu	23389	2811	6529
<b>Invention: CMOS DEVICE INTEGRATION FOR LOW EXTERNAL RESISTANCE</b>					
<u>COMMISSIONER FOR PATENTS:</u>				<b>RECEIVED CENTRAL FAX CENTER</b>	
Transmitted herewith is an amendment in the above-identified application.				<b>SEP 27 2004</b>	
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	<b>CLAIMS REMAINING AFTER AMENDMENT</b>	<b>HIGHEST # PREV. PAID FOR</b>	<b>NUMBER EXTRA CLAIMS PRESENT</b>	<b>RATE</b>	<b>ADDITIONAL FEE</b>
<b>TOTAL CLAIMS</b>	16 -	20 =	0 x	\$18.00	\$0.00
<b>INDEP. CLAIMS</b>	1 -	3 =	0 x	\$86.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
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P11LARGE/REV07

<b>AMENDMENT TRANSMITTAL LETTER (Large Entity)</b>				<b>Docket No.</b>	
Applicant(s): <b>Shreesh Narasimha, et al.</b>				<b>FIS920020200US2 (16106A)</b>	
<b>Application No.</b> 10/763,308	<b>Filing Date</b> January 23, 2004	<b>Examiner</b> Quang D. Vu	<b>Customer No.</b> 23389	<b>Group Art Unit</b> 2811	<b>Confirmation No.</b> 6529
<b>Invention: CMOS DEVICE INTEGRATION FOR LOW EXTERNAL RESISTANCE</b>					
<b><u>COMMISSIONER FOR PATENTS:</u></b>					
Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.					
<b>CLAIMS AS AMENDED</b>					
	<b>CLAIMS REMAINING AFTER AMENDMENT</b>	<b>HIGHEST # PREV. PAID FOR</b>	<b>NUMBER EXTRA CLAIMS PRESENT</b>	<b>RATE</b>	<b>ADDITIONAL FEE</b>
<b>TOTAL CLAIMS</b>	16 -	20 =	0 x	\$18.00	\$0.00
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<b>Multiple Dependent Claims (check if applicable)</b> <input type="checkbox"/>					\$0.00
<b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT</b>					\$0.00
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